	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	7119	(subopcode or microcode) and (digital adj signal adj processor or processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/19 12:19	
2	BRS	L2	5552	1 and instruction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/19 12:19	
3	BRS	L3	2410	2 and decoder	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/19 12:19	
4	BRS	L4	2405	3 and (memory or registers)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/19 12:20	
5	BRS	L5	3	4 and (integrating SAME (subopcodes or microcode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/19 12:21	